Specifications typical at 25 °C. HV = +400 V. Test Load = 0.47mH + 0.15Ω + 47nF each side to ground.

	OUTPUT CURRENT							
266	Current Mode Output (+/-A Peak, A rms) Pulse Duration / Off time (ms)							
10/ 400.14	(5.2)	500/500	400/400	00//0	- //-	476/4000		
HV 400 V +/- A Peak	(DC) 250	<u>500/500</u> 312	<u>100/100</u> 350	<u>20/40</u> 430	<u>5/15</u> 475	<u> </u>	+ or -Burst 475	
A rms	250	221	247	248	237	177	237	
UNIDIRECTIONAL CURRENT BURST Peak Current Output Burst Waveform Burst Frequency Duty Factor During Burst Burst Duration Interval			<u>+</u> 47 All p 50 F 0.5 100	5 A positive or all n Iz ms ms	egative pu	lses		
LOAD								
Current Mode			0.47	' mH + 0.15Ω				
OUTPUT VOLTAGE			<u>+</u> 350 pu	<u>+</u> 350 V pk across output terminals with 430 A pulses				
PARALLEL OPERATION operation			Amp	Amplifier may be connected for Master/Slave				
BANDWIDTH, SMALL SIGNAL			–3 c	IB @ 5 kHz				
SETTLING RESPONSE Current Error < 1%, 4.5 A Current Error < 0.2%, 0.9 A			Afte 150 260	After 450 A output ramp 150 μs 260 μs				
INPUT LIMITER Current Mode			Adju ±30	Adjustable ±30 to ±520 A				
GAIN Current Mode Adjustment Span Factory Preset to Accuracy Input 2			45 t 50 A ±0.5 2.5	o 65A/V \/V \$% A/V ±1%				
OUTPUT OFFSET Current Mode Adjustment Span Stability			Adju 1.0	Adjustable, factory set to zero 1.0 A				
La HV Su	ong Term (> 1 y ipply Sensitivity	/ear) /	< 80 < 0.) mA 5 mA/V				
PROPAGATION DELAY			35 µ Ioac	35 μs, independent of amplitude load dependent, adjustable range \pm 5μs typical				
EXTERNAL OFFSET			Use	r added to Inp	ut 2 (2.	5 A/V)		



IS

DC DRIFT Due to Self-Heating @ 250 A DC Offset < 10 mA/15 min < 25 mA/ 15 min Gain Due to Ambient Temperature Offset 1.2 mA/°C 30 ppm/°C Gain LINEARITY DC Endpoint, ±400 A < 0.01% **Total Harmonic Distortion** < 0.2% @ 200 Hz, 250 A rms **NOISE OUTPUT Current Mode Discrete Frequency** 10 Hz to 40 Hz < 200 µA rms 50 Hz to 60 Hz < 10µA rms 40 Hz to 10 kHz $< 5 \text{ x f } \mu \text{A rms}$, where f = frequency in Hz 50 - 200 kHz Ripple Load Current, 0 V Output < 0.8 mA Load Current, 200 V Output < 4 mA Amplifier Current, 0 V Output < 200 mA rms x C (where C= 0.047µF I = 10 mA) where C = wall filter capacitance in μ F each side to ground Random Total 0.1 Hz to 10 Hz < 1.6 mA p-p < 500 µA rms 10 Hz to 150 Hz < 1.6 mA rms 10 Hz to 5 kHz Step (popcorn) 0.1 Hz to 10 Hz < 0.8 mA p-p Voltage Mode, Open Load Random Total 10 Hz to 500 Hz 1.2 mV RMS 10 Hz to 10 kHz 5 mV RMS Ripple 50 - 200 kHz Each Output to Ground < 0.2 V rms @ 0V output 200 V Output, Differential < 0.6 V rms Discrete Frequency, 0 V output Each Output to Ground < 60 mV rms, 1 MHz to 5 MHz < 10 mV rms, 5 MHz to 100 MHz SIGNAL INPUTS Input 1 Differential Input 2 Differential Impedance $> 20 \text{ k}\Omega$ Max Input Voltage ±20 V either input or differential **Common Mode Rejection Ratio** 80 dB minimum, DC to 360 Hz **OUTPUT IMPEDANCE** $> 800\Omega$ DC 4 kHz to 6 kHz 10 Ω SWITCHING FREQUENCY 50 kHz Synchronization 1 MHz ±1% input or output, TTL Effect of Clock Loss Amplifier inhibits; low voltage power converters and PWM reference operate



MODEL 266 HIGH POWER AMPLIFIER

VOLTAGE MONITOR

Sensitivity Range Stability Source Impedance

CONFIGURATION MODULE

Accessibility Optional Modules Available

REMOTE ENABLE

FRONT PANEL SWITCHES

LOAD PROTECTION

Voltage or Current

Shutdown

Diode Clamps

FAULT PROTECTION

(latched unless otherwise stated)

LEDS

ENABLED STATUS OK DC OK DC FAULT INHIBIT OVERCURR OVERTEMP

Front BNC and rear D connector ±1 V/50 A ±0.5% ±12 V 0.1% 100 Ω Front BNC and rear D connector ±1 V/40 V ±1% ±12 V 0.1% 100 Ω 3 per axis-switchable, set gain and response for specific load Front TEST mode for short circuit burn-in. VOLTAGE CONTROL mode SLAVE mode. (Same for either current or voltage control, for parallel operation of amplifiers) TTL ground or 5 V opto-isolated input enables output Front panel INHIBIT switch must be off INHIBIT RESET Adjustable input limiter Soft start Current vs. time (Note 1) All four bridge arms open To +HV and ground Shutdown Power device failure Short Circuit Load Disconnected Overcurrent vs. time Temp. Fault High frequency overload (Note 2) Overtemperature at any of 11 measurement points Clock loss HV (not latched) Power supplies or internal voltages out of tolerance (not latched) Open Connector, internal (not latched, fan, current sensor, thermistor, display)

Green. Operating, no faults and INHIBIT switch is off Yellow. No faults and INHIBIT switch is off Green. All DC voltages within tolerance Red. One or more DC voltages out of tolerance Red. INHIBIT switch has been pushed. Red. Too much current for too long or too much HF output (Note 1.) Red. Heat sink, inductor, or diode too hot; 13 sensors

Note 1. Protection for current vs. time to spec. table.

2. Protection of high frequency due to internal oscillation or due to excessive high frequency demand (≥ 6 kHz at full output voltage swing).



TTL OUTPUTS STATUS OK ENABLE BUS

RESET

TEST CONNECTORS

POWER REQUIREMENTS

Fan Supply High Voltage Amplifier Shutdown Dissipation at 250 A DC Dissipation during 475 A peak Internal Capacitance, Each Amplifier

MECHANICAL

Size

Weight

Active Low Enabled and operating, no faults and INHIBIT switch is off Amplifier is enabled and operating (Controls slaves)

Front panel push switch or rear input for TTL lowor switch

Two rear 25-pin D connectors provide analog and TTL digital test points for factory tests and servicing

+28 VDC @ 6 A (for fans and internal voltages) +400 V < 55 V or > 425 V 3.6 kW 11.3 kW 38,000 μF

10.5" H x 19" W x 28.5" D, rack mount 26.7 cm H x 48.3 cm W x 72.4 cm D 114 lb., 51.7 kg

