Specifications typical at $25^{\circ} \mathrm{C} . \mathrm{HV}=+400 \mathrm{~V}$. Test Load $=0.47 \mathrm{mH}+0.15 \Omega+47 \mathrm{nF}$ each side to ground.

| $266$ <br> HV 400 V | OUTPUT CURRENT <br> Current Mode Output (+/-A Peak, A rms) Pulse Duration / Off time (ms) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
|  | (DC) | 500/500 | 100/100 | 20/40 | 5/15 | 170/1000 | + or -Burst |
| +/- A Peak | 250 | 312 | 350 | 430 | 475 | 430 | 475 |
|  | 250 | 221 | 247 | 248 | 237 | 177 | 237 |


| UNIDIRECTIONAL CURRENT BURST |  |
| :---: | :---: |
| Peak Current Output | $\pm 475$ A |
| Burst Waveform | All positive or all negative pulses |
| Burst Frequency | 50 Hz |
| Duty Factor During Burst | 0.5 |
| Burst Duration | 100 ms |
| Interval | 100 ms |
| LOAD |  |
| Current Mode | $0.47 \mathrm{mH}+0.15 \Omega$ |
| OUTPUT VOLTAGE | $\pm 350 \mathrm{~V}$ pk across output terminals with 430 A pulses |
| PARALLEL OPERATION operation | Amplifier may be connected for Master/Slave |
| BANDWIDTH, SMALL SIGNAL | -3 dB @ 5 kHz |
| SETTLING RESPONSE | After 450 A output ramp |
| Current Error < 1\%, 4.5 A | $150 \mu \mathrm{~s}$ |
| Current Error < 0.2\%, 0.9 A | $260 \mu \mathrm{~s}$ |
| INPUT LIMITER | Adjustable |
| Current Mode | $\pm 30$ to $\pm 520 \mathrm{~A}$ |
| GAIN |  |
| Current Mode Adjustment Span | 45 to 65A/V |
| Factory Preset to | $50 \mathrm{~A} / \mathrm{V}$ |
| Accuracy | $\pm 0.5 \%$ |
| Input 2 | 2.5 A/V $\pm 1 \%$ |
| OUTPUT OFFSET | Adjustable, factory set to zero |
| Current Mode Adjustment Span | 1.0 A |
| Stability |  |
| Long Term (> 1 year) | $<80 \mathrm{~mA}$ |
| HV Supply Sensitivity | $<0.5 \mathrm{~mA} / \mathrm{V}$ |
| PROPAGATION DELAY | $35 \mu \mathrm{~s}$, independent of amplitude load dependent, adjustable range $\pm 5 \mu \mathrm{~s}$ typical |
| EXTERNAL OFFSET | User added to Input 2 (2.5 A/V) |

EXTERNAL OFFSET
$\pm 475$ A
All positive or all negative pulses
50 Hz
100 ms
100 ms
$0.47 \mathrm{mH}+0.15 \Omega$
$\pm 350 \mathrm{~V}$ pk across output terminals with 430 A pulses

Amplifier may be connected for Master/Slave
-3 dB @ 5 kHz
After 450 A output ramp
$150 \mu \mathrm{~s}$
$260 \mu \mathrm{~s}$
Adjustable
$\pm 30$ to $\pm 520 \mathrm{~A}$

45 to 65A/V
50 A/V
$\pm 0.5 \%$
$2.5 \mathrm{~A} / \mathrm{V} \pm 1 \%$
Adjustable, factory set to zero
1.0 A
$<80 \mathrm{~mA}$
$<0.5 \mathrm{~mA} /$
$35 \mu \mathrm{~s}$, independent of amplitude

User added to Input 2 (2.5 A/V)

## DC DRIFT

Due to Self-Heating @ 250 A DC

Offset
Gain
Due to Ambient Temperature
Offset Gain

## LINEARITY

DC Endpoint, $\pm 400 \mathrm{~A}$
Total Harmonic Distortion

## NOISE OUTPUT

Current Mode
Discrete Frequency
10 Hz to 40 Hz
50 Hz to 60 Hz
40 Hz to 10 kHz
Ripple
Load Current, 0 V Output Load Current, 200 V Output Amplifier Current, 0 V Output

Random Total
0.1 Hz to 10 Hz

10 Hz to 150 Hz
10 Hz to 5 kHz
Step (popcorn) 0.1 Hz to 10 Hz

Voltage Mode, Open Load
Random Total
10 Hz to 500 Hz
10 Hz to 10 kHz
Ripple
Each Output to Ground 200 V Output, Differential
Discrete Frequency, 0 V output Each Output to Ground

SIGNAL INPUTS
Input 1
Input 2
Impedance
Max Input Voltage
Common Mode Rejection Ratio
OUTPUT IMPEDANCE
DC
4 kHz to 6 kHz
SWITCHING FREQUENCY
Synchronization
Effect of Clock Loss
$<10 \mathrm{~mA} / 15 \mathrm{~min}$
$<25 \mathrm{~mA} / 15 \mathrm{~min}$
$1.2 \mathrm{~mA} /{ }^{\circ} \mathrm{C}$
$30 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$

$$
\begin{aligned}
& <0.01 \% \\
& <0.2 \% \text { @ } 200 \mathrm{~Hz}, 250 \text { A rms }
\end{aligned}
$$

$<200 \mu \mathrm{~A}$ rms
$<10 \mu \mathrm{Arms}$
$<5 \times f \mu \mathrm{Arms}$, where $\mathrm{f}=$ frequency in Hz
$50-200 \mathrm{kHz}$
$<0.8 \mathrm{~mA}$
$<4 \mathrm{~mA}$
< 200 mA rms x C (where $\mathrm{C}=0.047 \mu \mathrm{~F} \quad \mathrm{I}=10 \mathrm{~mA}$ ) where $\mathrm{C}=$ wall filter capacitance in $\mu \mathrm{F}$ each side to ground
$<1.6$ mA p-p
$<500 \mu \mathrm{~A}$ rms
$<1.6 \mathrm{~mA}$ rms
$<0.8 \mathrm{~mA} \mathrm{p}-\mathrm{p}$
1.2 mV rms

5 mV rms
$50-200 \mathrm{kHz}$
$<0.2 \mathrm{~V}$ rms @ 0V output
$<0.6 \mathrm{~V}$ rms
$<60 \mathrm{mV}$ rms, 1 MHz to 5 MHz
$<10 \mathrm{mV}$ rms, 5 MHz to 100 MHz

Differential
Differential
$>20 \mathrm{k} \Omega$
$\pm 20 \mathrm{~V}$ either input or differential
80 dB minimum, DC to 360 Hz
$>800 \Omega$
$10 \Omega$
50 kHz
$1 \mathrm{MHz} \pm 1 \%$ input or output, TTL
Amplifier inhibits; low voltage power converters and PWM reference operate

| CURRENT MONITOR Front BNC and rear D connector |  |
| :---: | :---: |
| Sensitivity | $\pm 1 \mathrm{~V} / 50 \mathrm{~A} \pm 0.5 \%$ |
| Range | $\pm 12 \mathrm{~V}$ |
| Stability | 0.1\% |
| Source Impedance | $100 \Omega$ |
| VOLTAGE MONITOR | Front BNC and rear D connector |
| Sensitivity | $\pm 1 \mathrm{~V} / 40 \mathrm{~V} \pm 1 \%$ |
| Range | $\pm 12 \mathrm{~V}$ |
| Stability | 0.1\% |
| Source Impedance | $100 \Omega$ |
| CONFIGURATION MODULE <br> Accessibility | 3 per axis-switchable, set gain and response for specific load Front |
| Optional Modules Available | TEST mode for short circuit burn-in. |
|  | VOLTAGE CONTROL mode |
|  | SLAVE mode. (Same for either current or voltage control, for parallel operation of amplifiers) |
| REMOTE ENABLE | TTL ground or 5 V opto-isolated input enables output Front panel INHIBIT switch must be off |
| FRONT PANEL SWITCHES | INHIBIT |
|  | RESET |
| LOAD PROTECTION |  |
| Voltage or Current | Adjustable input limiter |
|  | Soft start |
| Shutdown | Current vs. time (Note 1) |
|  | All four bridge arms open |
| Diode Clamps | To +HV and ground |
| FAULT PROTECTION <br> (latched unless otherwise stated) | Shutdown |
|  | Power device failure |
|  | Short Circuit |
|  | Load Disconnected |
|  | Overcurrent vs. time |
|  | Temp. Fault |
|  | High frequency overload (Note 2) |
|  | Overtemperature at any of 11 measurement points |
|  | Clock loss |
|  | HV (not latched) |
|  | Power supplies or internal voltages out of tolerance (not latched) |
|  | Open Connector, internal (not latched, fan, current sensor, thermistor, display) |
| LEDS |  |
| ENABLED | Green. Operating, no faults and INHIBIT switch is off |
| STATUS OK | Yellow. No faults and INHIBIT switch is off |
| DC OK | Green. All DC voltages within tolerance |
| DC FAULT | Red. One or more DC voltages out of tolerance |
| INHIBIT | Red. INHIBIT switch has been pushed. |
| OVERCURR | Red. Too much current for too long or too much HF output (Note 1.) |
| OVERTEMP | Red. Heat sink, inductor, or diode too hot; 13 sensors |

Note 1. Protection for current vs. time to spec. table.
2. Protection of high frequency due to internal oscillation or due to excessive high frequency demand ( $\geq 6 \mathrm{kHz}$ at full output voltage swing).

TTL OUTPUTS
STATUS OK
ENABLE BUS

## RESET

## TEST CONNECTORS

## POWER REQUIREMENTS

Fan Supply
High Voltage
Amplifier Shutdown
Dissipation at 250 A DC
Dissipation during 475 A peak
Internal Capacitance, Each Amplifier

## MECHANICAL

Size
Weight

## Active Low

Enabled and operating, no faults and INHIBIT switch is off Amplifier is enabled and operating (Controls slaves)

Front panel push switch or rear input for TTL lowor switch

Two rear 25-pin D connectors provide analog and TTL digital test
points for factory tests and servicing
+28 VDC @ 6 A ( for fans and internal voltages) +400 V
$<55 \mathrm{~V}$ or $>425 \mathrm{~V}$
3.6 kW
11.3 kW

38,000 $\mu \mathrm{F}$
10.5" H x 19" W x 28.5" D, rack mount $26.7 \mathrm{~cm} \mathrm{H} \times 48.3 \mathrm{~cm} \mathrm{~W} \times 72.4 \mathrm{~cm}$ D
$114 \mathrm{lb} ., 51.7 \mathrm{~kg}$

